



Faculty of Computer Science CS6999 Reading Course Presentation

Using Behavioral Specification for Digital System Design

By

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As the size and complexity of digital systems increase, designing an efficient and fast digital system becomes a challenging task due to the low level abstraction of traditional hardware description languages, such as VHDL. Variations of software parallel programming languages are currently being studied to move digital system design, at the specification level, from structural to behavioral description.

This seminar introduces a parallel programming language OpenMP, a traditional hardware description language VHDL, and a high level hardware design language Handel-C. This seminar also discusses the conjunction between hardware programming and software programming. A brief digital design background is introduced for software developers.

STUDENTS ARE ENCOURAGED TO ATTEND
