



# Faculty of Computer Science CS6999 Reading Course Presentation

## A Survey of Techniques for the Co-verification of Hardware/Software Co-designed Systems

By

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*Monday, January 29<sup>th</sup>, 2007*

*3:35pm*

*ITC317*

This survey describes the research being performed and tools available for the verification of system designs for hardware-only and hardware/software co-designed systems. Formal and informal methods of verification are discussed along with an example of the use of the verification facilities included in the SystemC toolset.

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STUDENTS ARE ENCOURAGED TO ATTEND  
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