



Faculty of Computer Science 2007–2008 Seminar Series

Programming for Multi-Core Processors

By

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Performance increase of microprocessors has enjoyed exponential growth ever since they have been first introduced. The growth seems to have reached its limits. Vendors are now increasing performance by building chips with multiple cores. This presents new challenges for programmers—both at the operating system level as well as the application level. Some of these challenges will be discussed.

STUDENTS ARE ENCOURAGED TO ATTEND
