



Seminar Series

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**FACULTY OF
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The next generation of SAT solvers

By

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In past solvers for Boolean satisfiability (SAT solver) have become great success in dozens of areas as for example formal verification, automatic test pattern generation or logic synthesis. A reason for that was the development of several sophisticated SAT techniques as e.g. learning, efficient implications strategies and branch heuristics. However, common SAT solvers work on the Boolean level. Due to the increasing complexity of the considered problems (e.g. exponential growth of the design sizes in circuit verification) in the last years several approaches have been studied which lift the solve engine to higher levels of abstractions. Thus, a new generation of SAT solvers - namely word-level solvers and SAT modulo theories (SMT) - have been introduced. The talk gives a survey of the different kinds of SAT-like proof techniques. The main concepts of engines exploiting word-level descriptions or specialized theory solvers are introduced. In particular the word-level solver SWORD (SAT-like word level solver) is described. Applications in circuit verification and logic synthesis show how higher abstractions can be utilized during the solve process.

STUDENTS ARE ENCOURAGED TO ATTEND